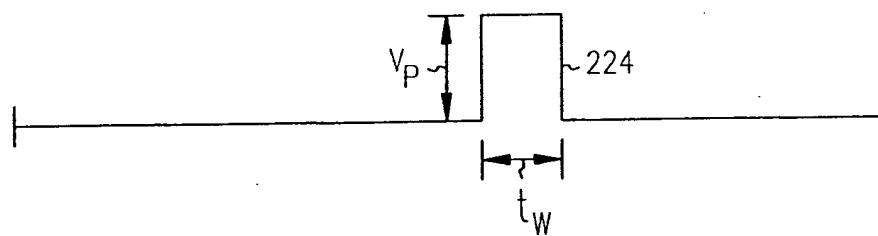
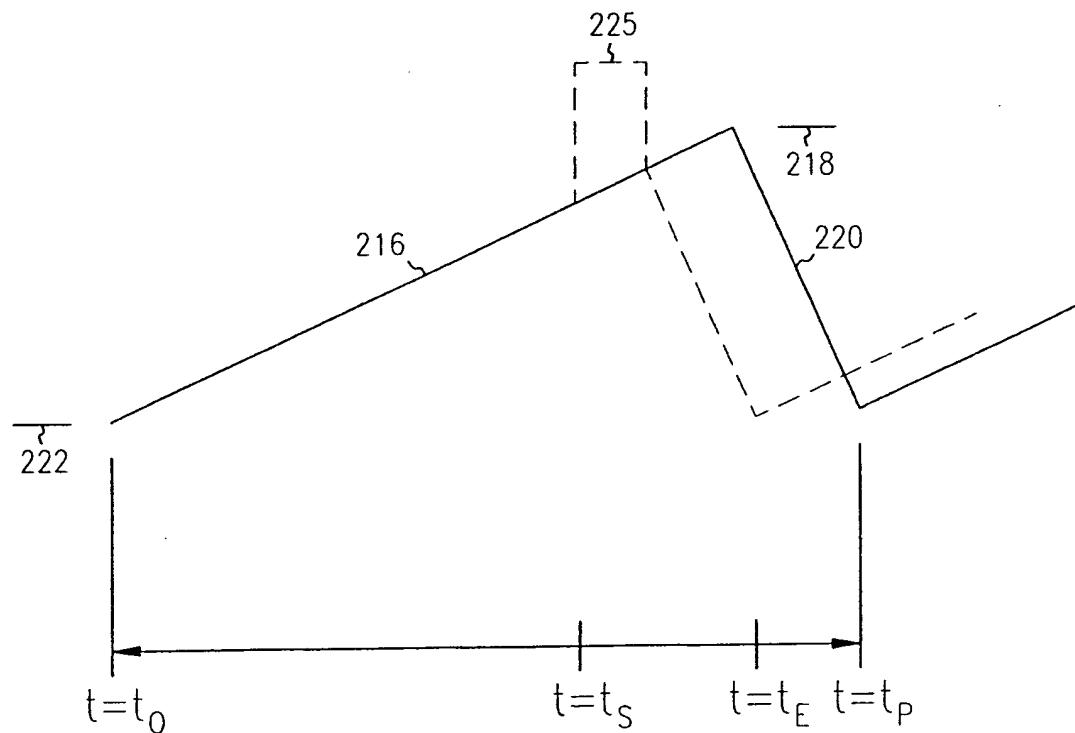


FIG. 1  
(PRIOR ART)

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD  
INVENTORS NAME: Brian N. Poindexter

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**FIG. 2**  
(PRIOR ART)

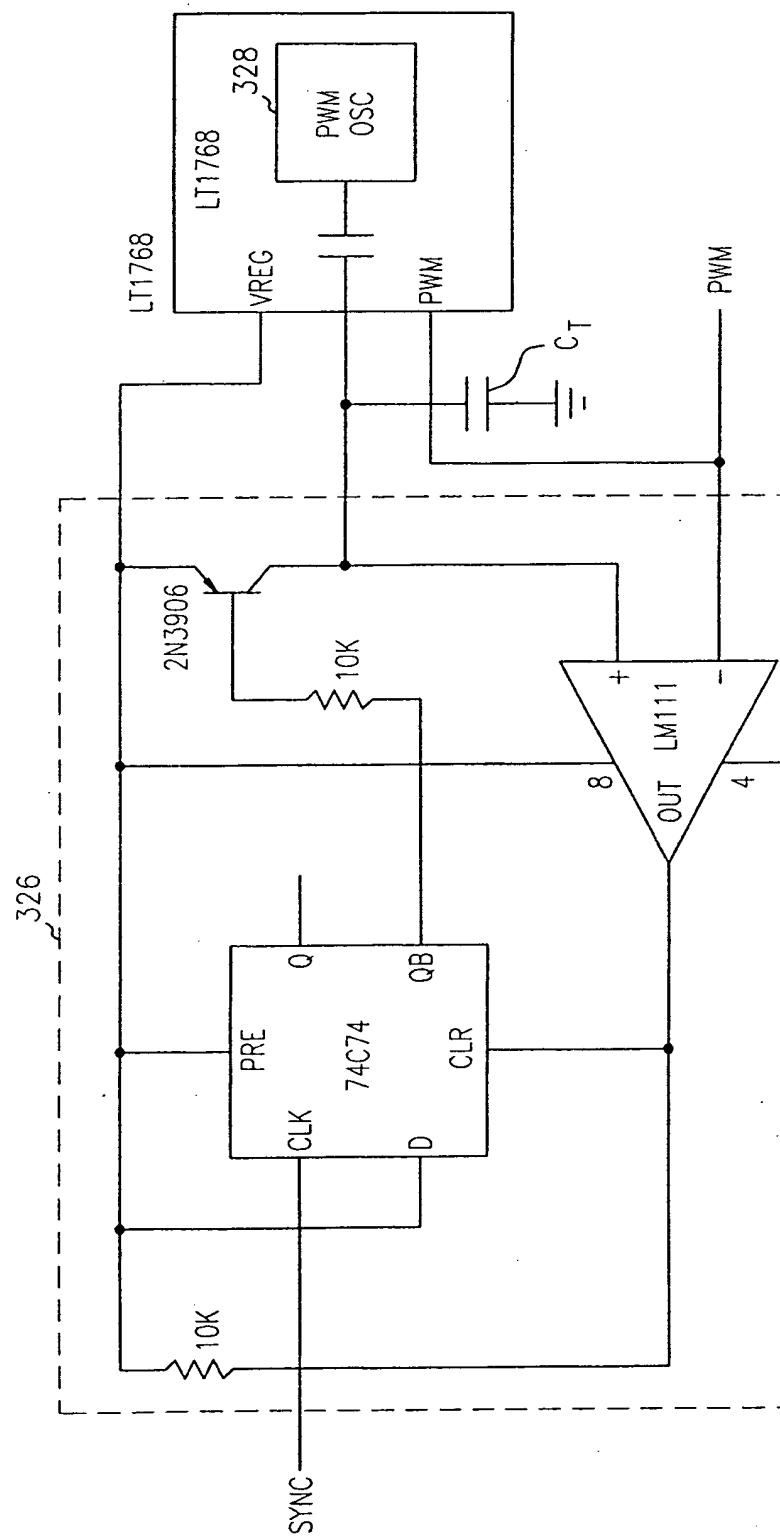


FIG. 3  
(PRIOR ART)

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD  
INVENTORS NAME: Brian N. Poindexter

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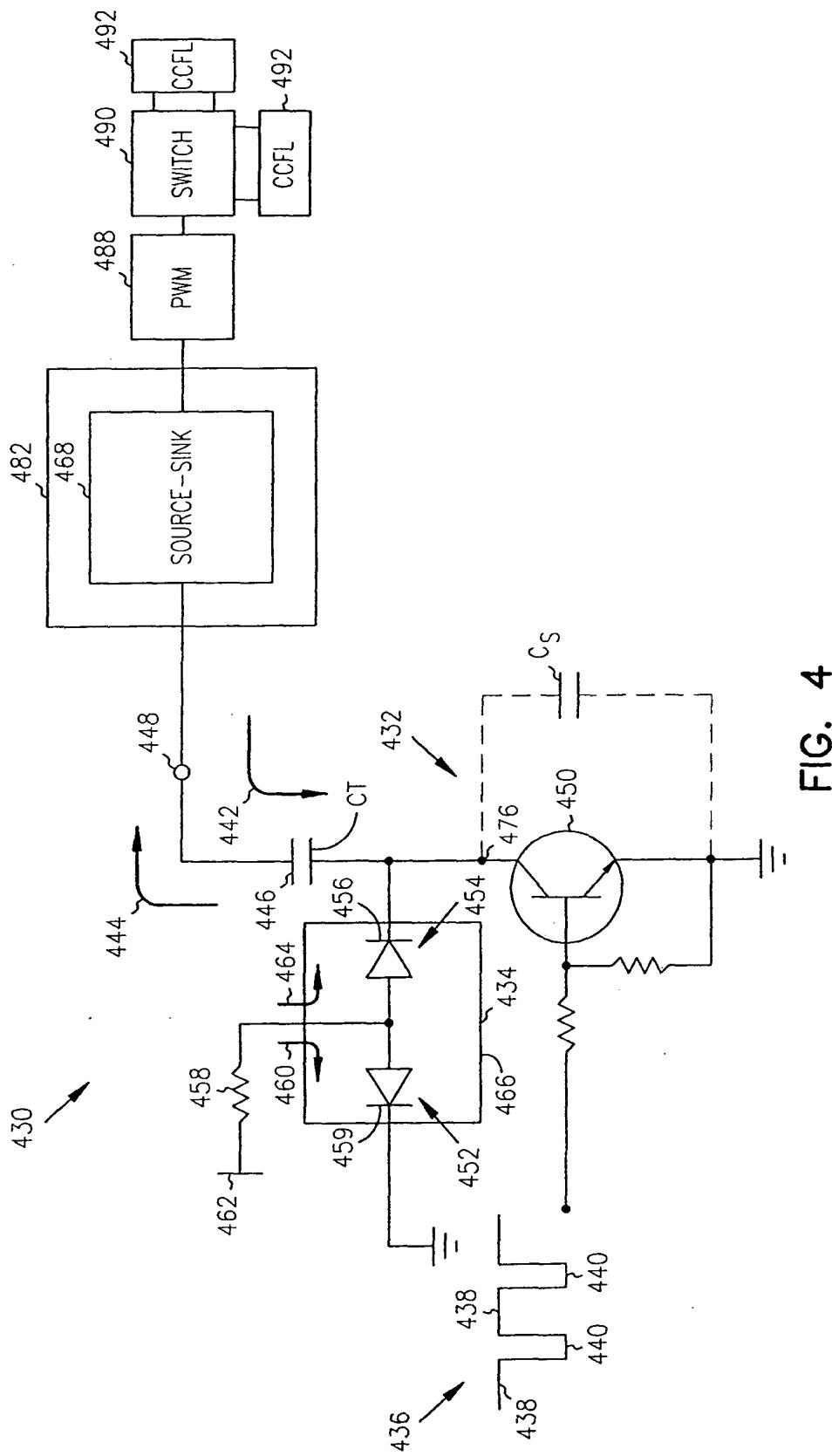


FIG. 4

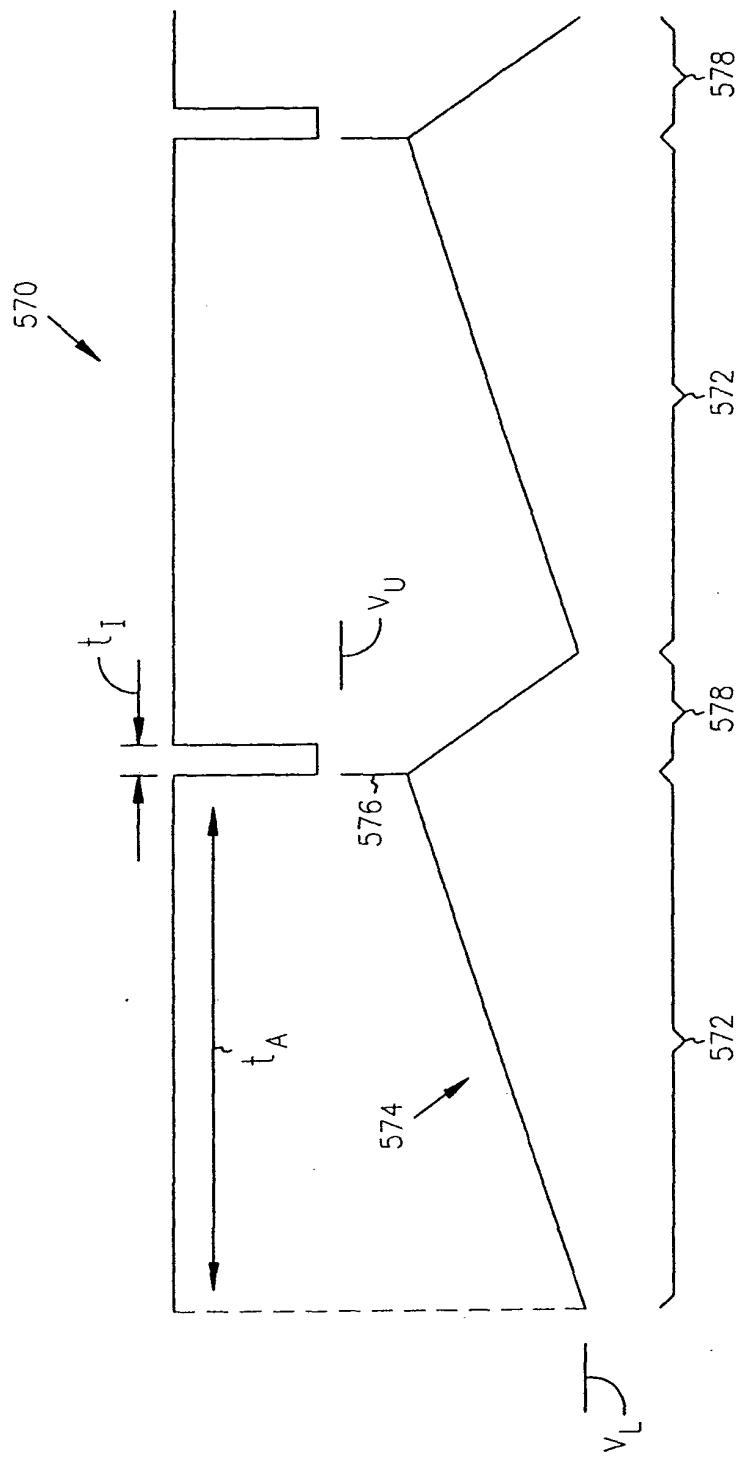


FIG. 5

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD  
INVENTORS NAME: Brian N. Poindexter

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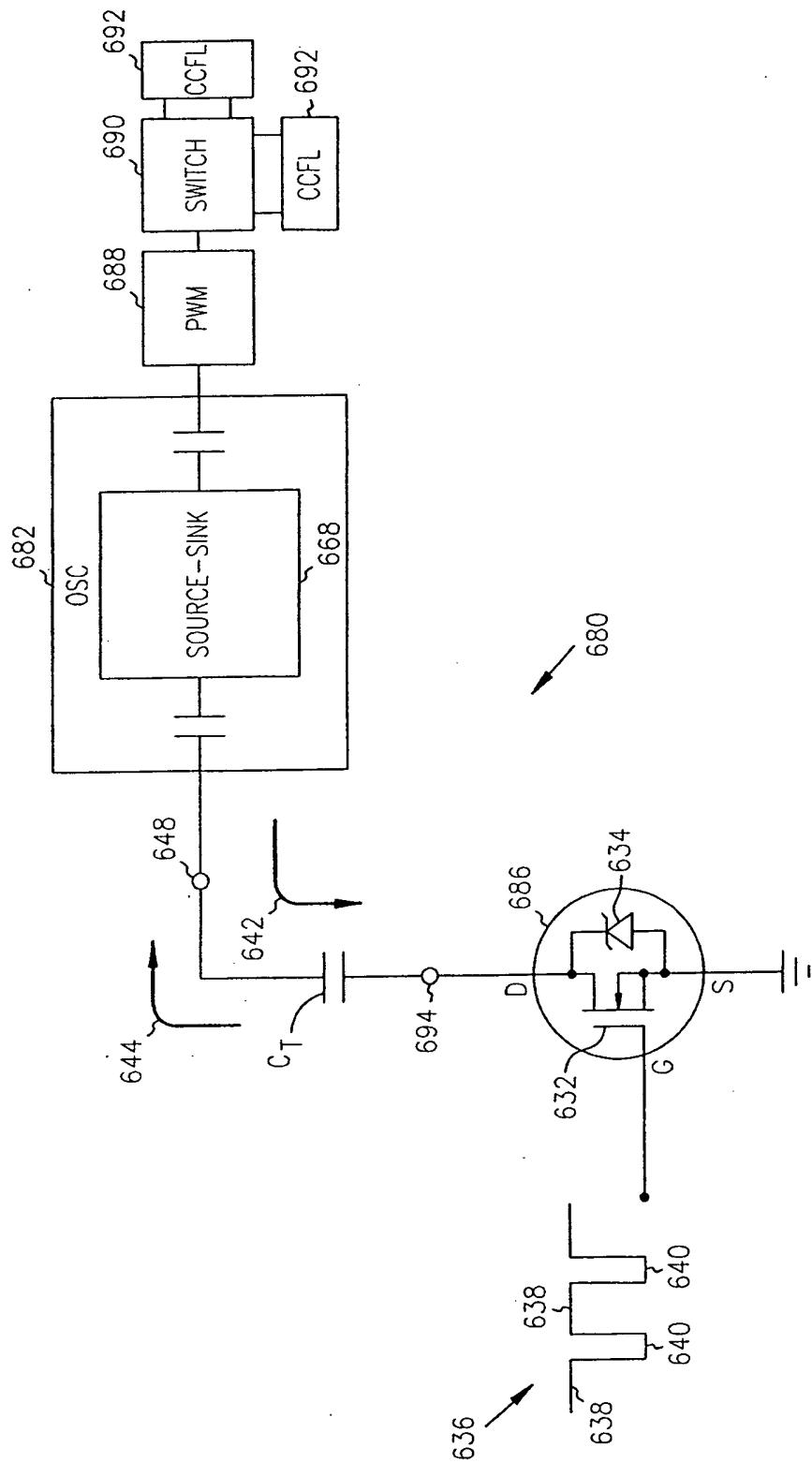


FIG. 6

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD  
INVENTORS NAME: Brian N. Poindexter

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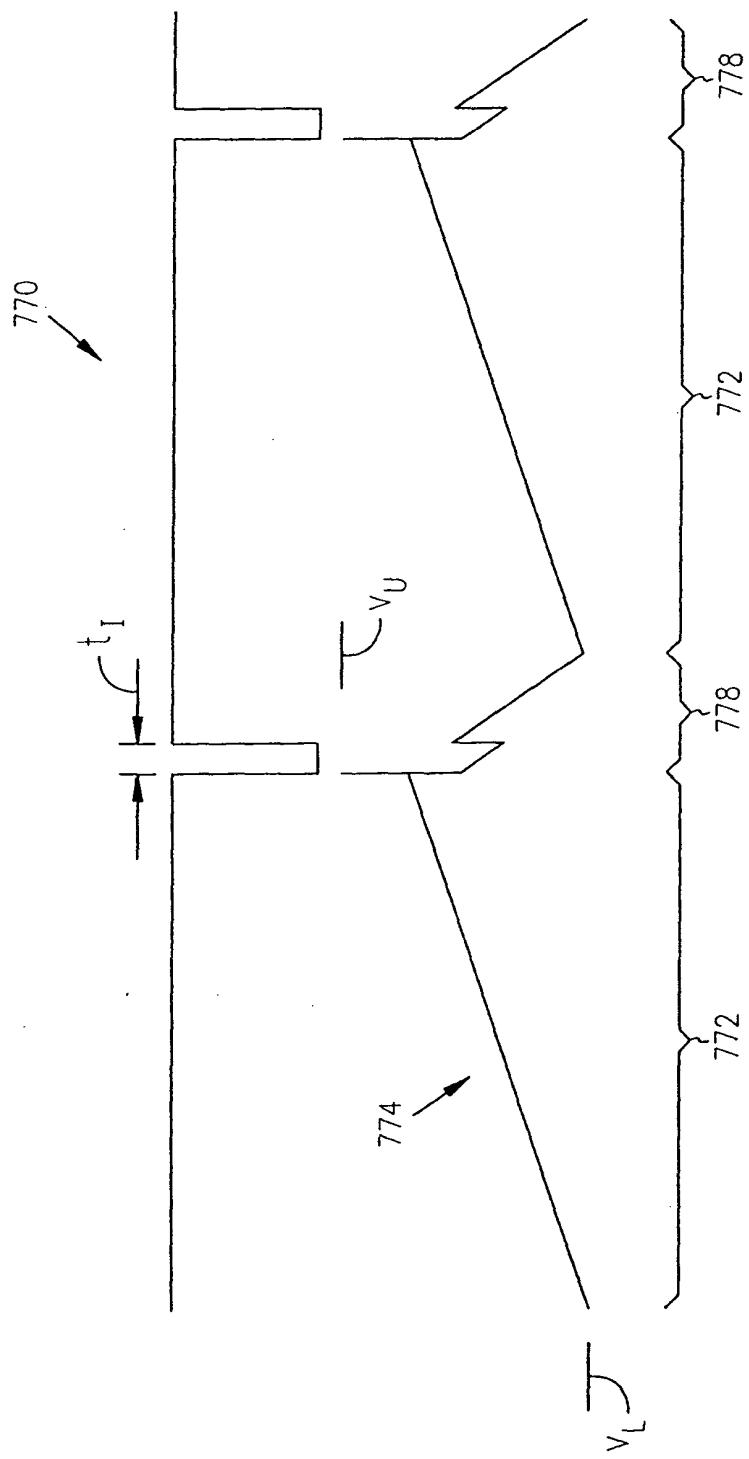


FIG. 7

TITLE: CIRCUIT SYNCHRONIZATION APPARATUS AND METHOD  
INVENTORS NAME: Brian N. Poindexter

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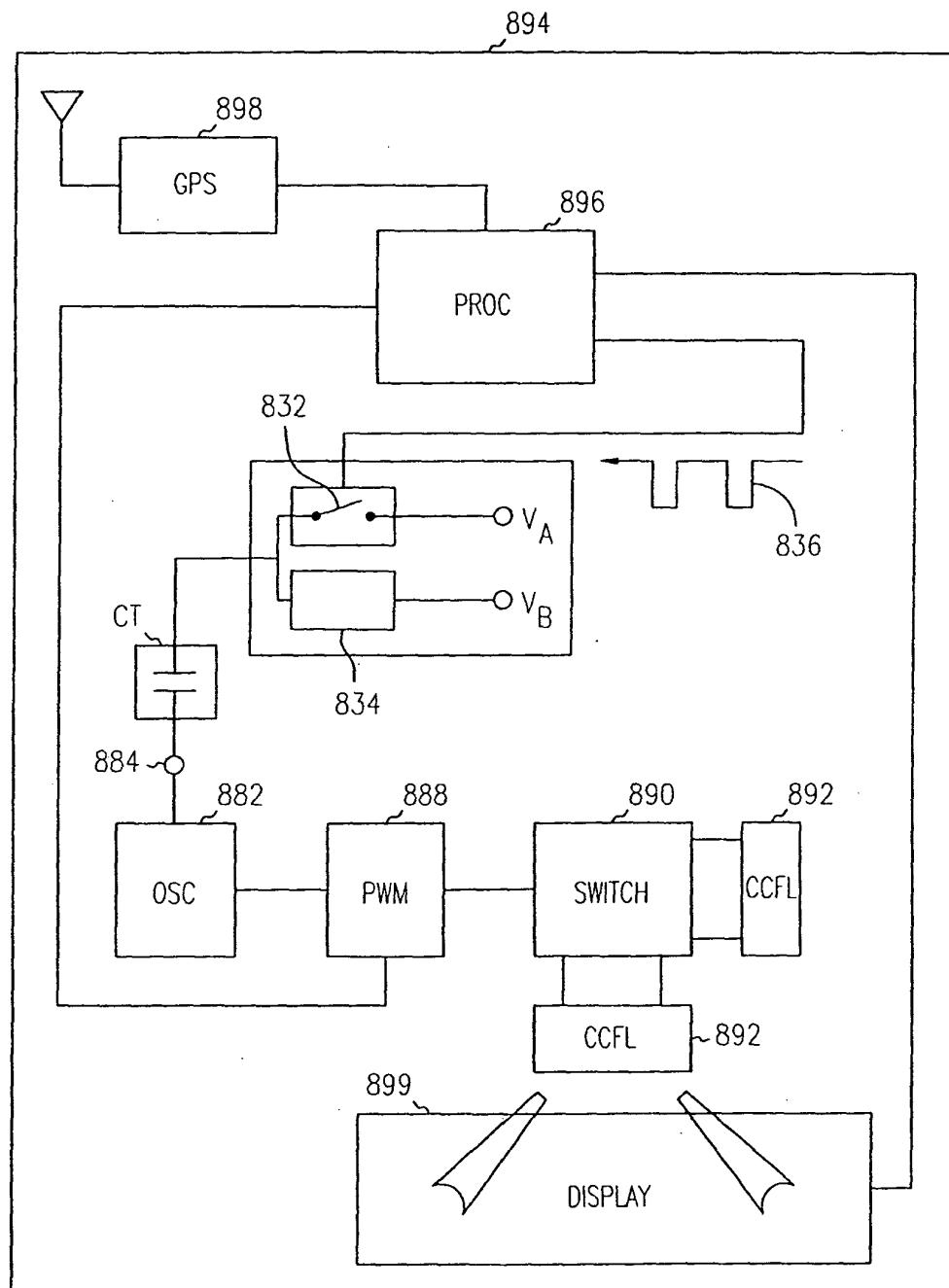


FIG. 8

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